



501.42869X00

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: CHAKIHARA, et al.
Serial No.: 10/629,733
Filed: July 30, 2003
For: A SEMICONDUCTOR MEMORY DEVICE AND A METHOD OF
MANUFACTURING THE SAME, A METHOD OF
MANUFACTURING A VERTICAL MISFET AND A VERTICAL
MISFET, AND A METHOD OF MANUFACTURING A
SEMICONDUCTOR DEVICE AND A SEMICONDUCTOR
DEVICE
Group: 2826
Examiner: F. ABRAHAM

**AMENDMENT, SECOND REQUEST FOR CLARIFICATION AND
REQUEST FOR INTERVIEW**

MS: AMENDMENTS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 31, 2005

Sir:

In response to the Office Action dated December 28, 2004, and in supplement to the Request for Clarification of Office Action filed on January 24, 2005, it is respectfully requested that this application be amended as listed below and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the Amendments.

Appendix A including a copy of the January 24, 2005 Request for

Clarification of Office Action and the PTO-dated receipt.

06/01/2005 SDENBOB1 00000047 10629733

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S.N. 10/629,733
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Dkt. 501.42869X00
Page 2 of 42